Semi-Automatic Writing Handler
Application Note

Applied for: All MCU

Padauk's Writer can write IC and calibrate IHRC simultaneously. When using semi-automatic writing handler to write IC, the golden pin should contact the IC pins. As a set of long bus wires are required to connect the golden pin with the OTP writer, power supply of IC may occasionally unstable and therefore affect the accuracy of IHRC calibration. In order to improve it, please refer to below diagram (For example: Writing PDK82C13 SSOP20) and connect a 0.01uF capacitor in between VDD and GND golden pins. Most of times, the VDD and GND golden pins are at two sides of the connector PCB and so a connecting wire may be required.

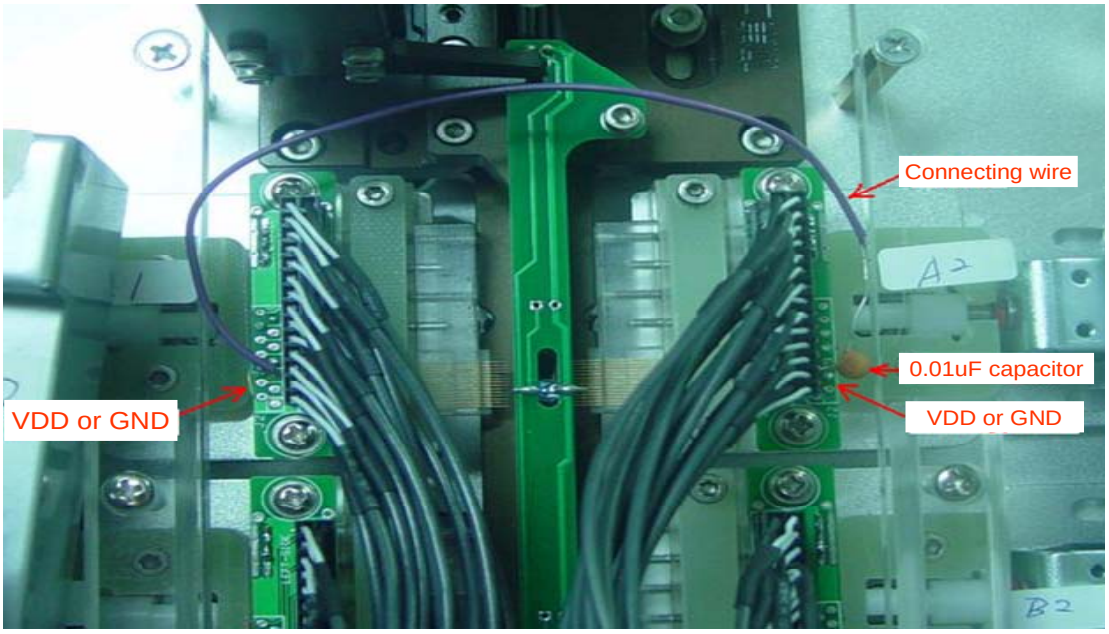
Attention must be paid on below,

(1) Once there is change on IC type or package, the position of the 0.01uF capacitor must be adjusted correspondingly. Otherwise, writing will be fail.

(2) IDE 0.46 or above versions must be used for adding this 0.01uF capacitor. Please update your software through the website of Padauk Technology at (http://www.padauk.com.tw/).

To avoid frequent repositioning the 0.01uF capacitor for writing different IC or packages, user can also consider welding the 0.01uF capacitor at the handler connector side of the bus wires. At that time, each IC type or package is corresponding to one set of bus wires, and the user can select the correct set of bus wires in accordance with the IC type or package.

When using semi-automatic handler to write PDK82C13 SSOP20, the position of 0.01uF capacitor is shown as below diagram:



If you have further questions to the application, please consult to our agent at your nearest location or contact us at fae@padauk.com.tw